

**Amendment and Response**

Applicant: Daniel Kehrer et al.

Serial No.: 10/520,805

Filed: October 13, 2005

Docket No.: 1432.113.101/P29564

Title: INTEGRATED CIRCUIT ARRANGEMENT

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**IN THE CLAIMS**

Please cancel claims 9, 18, and 20-23.

Please add claims 24 and 25.

Please amend claims 2, 6, 12, 15, 16, and 19 as follows:

1. (Cancelled)
2. (Currently Amended) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;  
at least one first data output connection; and  
at least one first inductance connected between the at least one first output connection and the at least one first data output connection, wherein at least one of the at least one first inductance is a monolithically integrated inductance.
3. (Previously Presented) The integrated circuit arrangement of claim 2, comprising:  
wherein the output circuit has a second output connection and a second data output connection; and  
at least one second inductance connected between the second output connection and the second data output connection.
4. (Previously Presented) The integrated circuit arrangement of claim 3, comprising where the first inductance is in a form such that it forms a first frequency filter having a prescribed frequency band together with the first data output connection, and the second inductance is in a form such that it forms a second frequency filter having the prescribed frequency band together with the second data output connection.
5. (Previously Presented) The integrated circuit arrangement of claim 4, comprising where

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the prescribed frequency band is in a range from 1 GHz to 100 GHz.

6. (Currently Amended) The integrated circuit arrangement of ~~claims~~ claim 2, comprising:  
a plurality of frequency filters coupled in series between the at least first output connection and the at least first data output connection.

7. (Previously Presented) The integrated circuit arrangement of claim 3, comprising where the output circuit is set up such that a differential data signal can be provided at the first output connection and at the second output connection.

8. (Previously Presented) The integrated circuit arrangement of claim 7, comprising where the at least one first inductance is coupled to the at least one second inductance.

9. (Cancelled)

10. (Previously Presented) The integrated circuit arrangement of claim 2, comprising where the output circuit has a differential amplifier.

11. (Previously Presented) The integrated circuit arrangement of claim 2, comprising where the output circuit has a multiplexer.

12. (Currently Amended) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;  
a first data output connection;  
a first inductance connected between the first output connection and the first data output connection; and  
wherein the output circuit has a second output connection and a second data output

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connection and a second inductance connected between the second output connection and the second data output connection,

wherein at least one of the inductances is a monolithically integrated inductance.

13. (Previously Presented) The integrated circuit arrangement of claim 12, comprising where the first inductance is configured such that it forms a first frequency filter having a prescribed frequency band together with the first data output connection, and the second inductance is configured such that it forms a second frequency filter having the prescribed frequency band together with the second data output connection.

14. (Previously Presented) The integrated circuit arrangement of claim 13, comprising where the prescribed frequency band is in a range from 1 GHz to 100 GHz.

15. (Currently Amended) An integrated circuit arrangement comprising:  
an output circuit having at least one first output connection which can provide a data signal;

at least one first data output connection; and

at least one first inductance connected between the at least one first output connection and the at least one first data output connection, wherein at least one of the at least one first inductance is a monolithically integrated inductance; and

a plurality of frequency filters coupled in series between the at least first output connection and the at least first data output connection.

16. (Currently Amended) The integrated circuit arrangement of claim 15, wherein the output circuit has a second output connection and a second data output connection and at least one second inductance connected between the second output connection and the second data output connection, comprising where the output circuit is set up such that a differential data signal can be provided at the first output connection and at the second output connection.

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17. (Previously Presented) The integrated circuit arrangement of claim 16, comprising where the at least one first inductance is coupled to the at least one second inductance.

18. (Cancelled)

19. (Currently Amended) The integrated circuit arrangement of claim ~~18~~17, comprising where the output circuit has a differential amplifier.

20-23. (Cancelled)

24. (New) The integrated circuit arrangement of claim 3, wherein at least one of the at least one second inductance is a monolithically integrated inductance.

25. (New) The integrated circuit arrangement of claim 16, wherein at least one of the at least one second inductance is a monolithically integrated inductance.